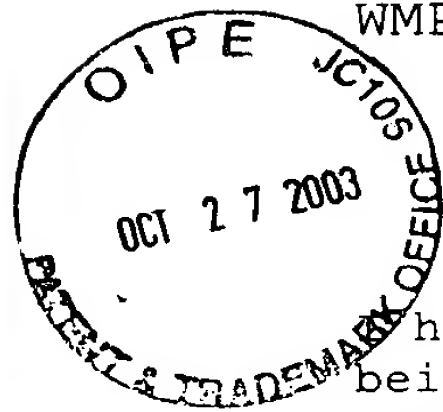


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WMP-IFT631



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Date: October 20, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/943,589 Confirmation No.: 1967
Applicant : Rainald Sander
Filed : August 30, 2001
Art Unit : 2829
Examiner : Paresh H. Patel
Title : Circuit Configuration for Detecting the Current in a Load Transistor
Docket No. : WMP-IFT631
Customer No. : 24131

A M E N D M E N T

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S i r :

Responsive to the Office Action dated June 18, 2003 kindly amend the above-identified application as follows:

Amendments to the Specification begin on page 3 of this paper.

10/29/2003 SSITHIB1 00000077 09943589

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110.00 OP

Appl. No. 09/943,589
Amdt. Dated October 20, 2003
Reply to Office Action of June 18, 2003

Amendments to the Claims are reflected in the listing of claims which begins on page 5 of this paper.

Amendments to the Drawings begin on page 14 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 15 of this paper.

An **Appendix** including amended drawing figures is attached following page 22 of this paper.